IN THE CLAIMS

I. Cancel Claims 1 through 20, without prejudice.

II. Add the following new Claims.

~~21.~~ An apparatus for receiving multiple data streams, the apparatus comprising:

(a) a first switch connected to a first input and having a first switch output, the first switch adapted to be selectively enabled for passing a first stream of data signals from the first input to the first switch output, the first stream of data signals including first channel data;

(b) a second switch connected to a second input and having a second switch output, the second switch adapted to be selectively enabled for passing a second stream of data signals from the second input to the second switch output, the second stream of data signals including second channel data different from the first channel data;

(c) a data stream junction connected to the first switch output and the second switch output and having a junction output; and

(d) a controller for receiving a channel select input related to a desired channel output to be formed from one of the first channel data or second channel data, and, in response to the channel select input, for enabling the one of the first switch or the second switch which receives the stream of data including the channel data from which the desired channel output is to be formed.

~~22.~~ The apparatus of Claim ~~21~~ further comprising:

(a) a signal processor connected to receive data signals from the data stream junction; and

(b) wherein the controller controls the operation of the signal processor to produce the desired channel output from data signals received from the data stream junction.

23. The apparatus of Claim 22 further including a memory device, the memory device storing:

(a) first channel output information, the first channel output information including (i) first signal processing information to control the processing of the first channel data and (ii) first signal input information indicating the switch through which the first channel data is received; and

(b) second channel output information, the second channel output information including (i) second signal processing information to control the processing of the second channel data and (ii) second signal input information indicating the switch through which the second channel data is received.

24. The apparatus of Claim 23 wherein the memory device stores additional channel output information including (i) additional processing information to control the processing of additional channel data and (ii) additional signal input information indicating the switch through which the respective additional channel data is received.

5. 2

~~25.~~ The apparatus of Claim ~~22~~ wherein the signal processor includes:

(a) a tuner;

(b) a demodulator;

(c) a forward error correction decoder; and

(d) a demultiplexer/format decoder.

6. 1

~~26.~~ The apparatus of Claim ~~21~~ wherein the data stream junction comprises:

(a) an impedance matching amplifier.

7. 1

~~27.~~ The apparatus of Claim ~~21~~ wherein:

(a) the first input receives signals on a plurality of first carrier frequencies; and

(b) the second input receives signals on at least one of the first carrier frequencies.

~~28.~~ An apparatus for receiving multiple data streams, the apparatus comprising:

(a) a plurality of input paths, each respective input path for carrying a different data stream;

(b) a switching structure associated with the plurality of input paths for selectively blocking the respective data stream on each different input path; and

(c) a controller for receiving a channel select input related to a desired channel output to be formed from data included in one of the different data streams, and for responding to the channel select input by blocking at least one of the

plurality of data streams which does not include the channel data from which the desired channel output is to be formed.

9. ~~29.~~ The apparatus of Claim 8 ~~28~~ further comprising:

(a) a signal processor; and

(b) wherein the controller controls the operation of the signal processor to produce the desired channel output from the data included in one of the different data streams.

Cancel

10. ~~30.~~ The apparatus of Claim ~~29~~ further comprising a memory device for storing channel output information for each different channel output which may be produced from the plurality of data streams, the channel output information for each respective channel output including signal processing information to control the signal processor in processing the respective channel data, and signal input information indicating the input path on which the respective channel data is carried.

11. ~~31.~~ The apparatus of Claim ~~30~~ 10 wherein the channel output information for each respective channel output is related to a unique channel identifier in the memory device.

12. ~~32.~~ The apparatus of Claim ~~29~~ 9 wherein the signal processor includes:

(a) a tuner;

(b) a demodulator;

19

A

(c) a forward error correction decoder; and

(d) a demultiplexer/format decoder.

13. ~~33.~~ The apparatus of Claim ~~28~~ 8 wherein:

(a) each data stream comprises signals from a different antenna.

14. ~~34.~~ The apparatus of Claim ~~28~~ 8 wherein:

(a) at least two of the data streams include signals on a common carrier frequency.

15. ~~35.~~ A method for receiving multiple data streams, the method comprising the steps of:

(a) directing a plurality of different data streams each along a different input path to a signal processor, each the data stream including channel data for producing a respective channel output;

(b) receiving a channel select input related to a desired channel output comprising a particular one of the channel outputs; and

(c) responding to the channel select input by blocking at least one of the data streams which does not include channel data from which the desired channel output is to be produced.

36. The method of Claim 35 further comprising the step of:

(a) storing channel output information for each different channel output which may be produced from the plurality of data streams, the channel output information

for each respective channel output including signal processing information to control the signal processor in processing the respective channel data, and signal input information indicating the input path on which the respective channel data is carried.

16. ~~37.~~ The method of Claim ~~36~~ 15 further comprising the steps of:

(a) in response to the channel select input, accessing the stored channel output information for the desired channel output; and

(b) controlling the operation of the signal processor with the signal processing information for the desired channel output.

17. ~~38.~~ The method of Claim ~~37~~ 16 further comprising the step of:

(a) utilizing a channel identifier uniquely associated with the desired channel output in accessing the channel output information for the desired channel output.

18. ~~39.~~ The method of Claim ~~38~~ 15 wherein:

(a) each data stream utilizes at least one common carrier frequency.

19. ~~40.~~ The method of Claim ~~37~~ 16 wherein the step of controlling the operation of the signal processor includes the steps of:

(a) tuning the signal processor to the carrier frequency of the channel data for producing the desired channel output;

(b) demodulating the signals at that carrier frequency; and

(c) decoding the demodulated signals to identify and select the channel data.

REMARKS

The Applicants respectfully request consideration and allowance of Claims 21 through 40 in light of the above amendments and the arguments set forth below.

The Applicants appreciate the Examiner's indication in the office action that Claims 2-3, 9-10, and 17-18 were directed to allowable subject matter.

The objection to the form of the drawings is noted. Formal drawings will be submitted upon notice of allowance.

I. The Amendments

The disclosure is amended above to more clearly describe the structure of the invention and the various types of signals and data utilized in the invention. The changes are definitional in nature and do not introduce new matter.

The original claims are canceled and replaced with new Claims 21-40. The Applicants believe the new claims clearly distinguish over the prior art cited in the first office action and overcome the Section 112 objections.

II. The Section 112 Rejections and Objection to Drawings

The Examiner rejected Claims 1-7 under 35 U.S.C. 112, first paragraph, and objected to the drawings based on a typographical error appearing in Claim 1. In particular, Claim 1